TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process) Silicon NPN Epitaxial Type (PCT Process)

# HN4B04J

Audio Frequency General Purpose Amplifier Applications **Driver Stage Amplifier Applications** Switching application

#### Q1:

Excellent hFF linearity

:  $h_{FE(2)}$  =25 (Min.) at  $V_{CE}$  = -6V  $I_{C}$  = -400mA

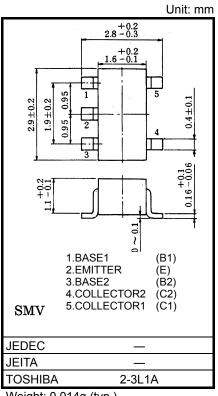
#### Q2:

Excellent hFF linearity

:  $h_{FE(2)} = 25$  (Min.) at  $V_{CE} = 6V I_C = 400 mA$ 

### Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-35	V
Collector-emitter voltage	V <sub>CEO</sub>	-30	V
Emitter-base voltage	V <sub>EBO</sub>	-5	V
Collector current	IC	-500	mA



Weight: 0.014g (typ.)

#### Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	35	V
Collector-emitter voltage	$V_{CEO}$	30	<b>V</b>
Emitter-base voltage	V <sub>EBO</sub>	5	٧
Collector current	Ic	500	mA

#### Q1,Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> *	300	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	<b>−55~150</b>	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

<sup>\*</sup>Total rating. Power dissipation per element should not exceed 200mW.

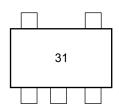
### Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	_	$V_{CB} = -35V$ , $I_E = 0$	_	_	-100	nA
Emitter cut-off current	I <sub>EBO</sub>	_	$V_{EB} = -5V, I_C = 0$	_	_	-100	nA
DC current gain	h <sub>FE(1)</sub>	_	$V_{CE} = -1V, I_{C} = -100 \text{mA}$	70	_	240	
	h <sub>FE(2)</sub>	_	$V_{CE} = -6V, I_{C} = -400 \text{mA}$	25	_	_	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	_	I <sub>C</sub> = -100mA, I <sub>B</sub> = -10mA	_	-0.1	-0.25	V
Base-Emitter Voltage	V <sub>BE</sub>	_	$V_{CE} = -1V, I_{C} = -100 \text{mA}$	_	-0.8	-1.0	V
Transition frequency	f <sub>T</sub>	_	$V_{CE} = -6V, I_{C} = -20mA$	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	$V_{CB} = -6V$ , $I_E = 0$ , $f = 1MHz$	_	13	_	pF

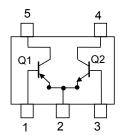
# Q2 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	_	V <sub>CB</sub> = 35V, I <sub>E</sub> = 0	_	_	100	nA
Emitter cut-off current	I <sub>EBO</sub>	_	V <sub>EB</sub> = 5V, I <sub>C</sub> = 0	-	-	100	nA
DC current gain	h <sub>FE(1)</sub>	_	V <sub>CE</sub> = 1V, I <sub>C</sub> = 100mA	70	-	240	
	h <sub>FE(2)</sub>	_	V <sub>CE</sub> = 6V, I <sub>C</sub> = 400mA	25	_	_	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	_	I <sub>C</sub> = 100mA, I <sub>B</sub> = 10mA	1	0.1	0.25	V
Base-Emitter Voltage	V <sub>BE</sub>	_	V <sub>CE</sub> = 1V, I <sub>C</sub> = 100mA	ı	0.8	1.0	V
Transition frequency	f <sub>T</sub>	_	$V_{CE}$ = 6V, $I_{C}$ = 20mA	ı	300	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	$V_{CB} = 6V, I_{E} = 0, f = 1MHz$	1	7	_	pF

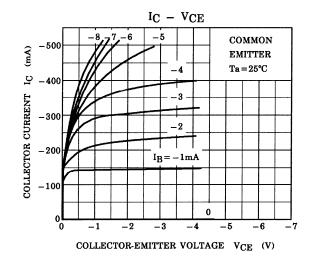
# Marking

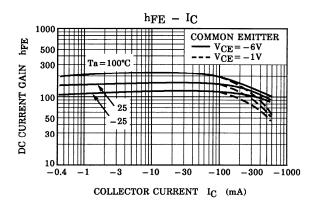


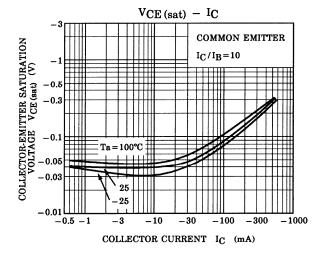
# **Equivalent Circuit (Top View)**

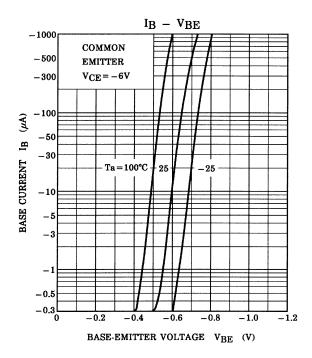


### Q1 (PNP transistor)



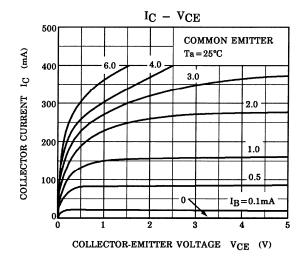


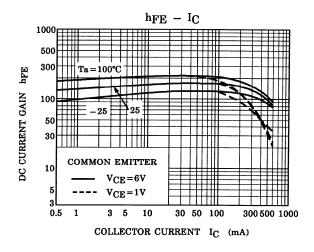


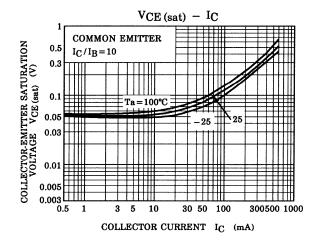


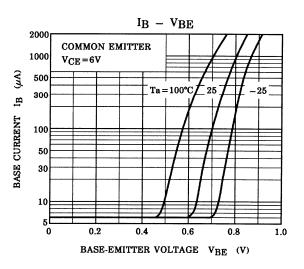
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### Q2 (NPN transistor)

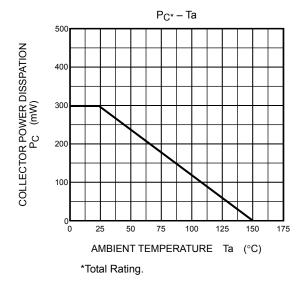








# (Q1,Q2 Common)



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